CS4290/CS 6290 High-Performance Computer Architecture

School of Computer Science Georgia Institute of Technology

8/28Performance, metrics, benchmarks39/2ILP, Dependences, and register renaming49/9Instruction scheduling and instruction commit59/16Branch prediction and speculative execution69/23 9/25Predication and VLIW	Due	Ch.1	
8/21Basic pipelining28/26IEEE floating points8/28Performance, metrics, benchmarks39/2ILP, Dependences, and register9/4renaming49/9Instruction scheduling and9/11instruction commit59/16Branch prediction and9/18speculative execution69/23Predication and VLIW9/2579/3079/30Static exploitation of ILP10/2Interrupts and exceptions910/14Caches		(n)	
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9/4renaming49/9Instruction scheduling and instruction commit59/16Branch prediction and speculative execution69/23Predication and VLIW 9/2579/30Static exploitation of ILP 10/2810/7Interrupts and exceptions 10/9910/14Caches	Information(8/28)	Ch1	Prof. Vuduc
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59/16Branch prediction and speculative execution69/23Predication and VLIW9/2579/3079/30Static exploitation of ILP 10/2810/7Interrupts and exceptions 10/9910/14Caches			
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10/2 1 8 10/7 Interrupts and exceptions 10/9 Mid-term 9 10/14 Caches	HW#2 due (10/2)	App G	Term project
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10/9 Mid-term 9 10/14 Caches		App A	Mid-term
9 10/14 Caches		· - PP · · ·	
		Ch5	Fall recess
		Chi	period
10 10/21 Memory	Lab #2 due	Ch5/App C	pence
	(10/23)	ensimpp e	
11 10/28 Multiprocessors	(10/20)	Ch4	-
10/30		Chi	
	Hw #3 due	Ch4	-
	(11/4)	Chi	
13 11/11 Interconnection networks, Storage	(·)	Ch6/App E	+
11/13			
14 11/18 Case study: Pentium			1
11/20 Case study: 1 cilitaria 11/20 Case study: G80			
	Lab #3 due		Thanksgiving
	(11/24)		(11/27)
	HW #4 due		Project due
	(12/2)		
Final	× · -/		Final exam

Instructor: Prof. Hyesoon Kim TA: Aemen Lodhi